#### SARANATHAN COLLEGE OF ENGINEERING

**Department of Electrical & Electronics Engineering** 

#### **FDTP on POWER ELECTRONICS**

Sponsored by

# Centre for Faculty Development Anna University, Chennai

# **REGISTRATION FORM**

Name:	
Age & Gender:	
Educational Qualification:	
Designation:	
Institution:	
Teaching Experience:	
Address for Communication:	
Mobile:	
E-mail id:	
Accommodation Required:	Yes / No

## **DECLARATION**

I agree to abide by the rules and regulations governing the FDTP. I shall attend the training programme for the entire duration, if selected.

# Signature of the Applicant

Dr./Mr./Ms.\_\_\_\_\_ is a faculty member / research scholar of our institution and will be permitted to attend the seminar, if selected.

Signature & Seal of the Head of the Institution

#### REGISTRATION:

- Faculty members of Anna University affiliated institutions are eligible to register.
- No registration fee for the participants
- As the maximum number of participants is restricted to 25 by CFD, Anna University, selection of participants will be on first come first serve basis.
- No TA / DA will be provided.
- Participant(s) may use the registration form available in the brochure or photocopy of the form or the downloaded form available in the web www.saranathan.ac.in

# **ACCOMMODATION:**

Accommodation will be provided to the outstation male participants in the college hostel free of cost subject to availability.

#### **SELECTION & INTIMATION:**

All the selected participants will be intimated through e-mail as well as over phone.

#### **Communication Details:**

Mr. R.Balasubramanian,
Associate Professor,
Co-ordinator,
FDTP on Power Electronics,
Department of Electrical & Electronics Engg.,
Saranathan College of Engineering,
Venkateswara Nagar, Panjapur,

Trichy - 620 012

email: balasubramanian-eee@saranathan.ac.in

Mob. No.: 9894091922

### SEVEN DAYS FDTP

ON

# POWER ELECTRONICS

23<sup>rd</sup> - 29<sup>th</sup> June 2014

Co-ordinator:
R.BALASUBRAMANIAN



Sponsored by

# Anna University Chennai

Organized by



Department of Electrical & Electronics
Engineering
(Accredited by NBA)

Saranathan College of Engineering
(Approved by AICTE & Affiliated to Anna University)
Venkateswara Nagar, Panjapur
Tiruchirapalli 620012

# **ORGANIZING COMMITTEE**

# **CHIEF PATRON:**

Prof.Dr.M.Rajaram Vice-Chancellor Anna University Chennai

# PATRON:

Dr.S.Ganesan Registrar Anna University Chennai

# **CHAIR:**

Dr.D.Mohan Director, CFD

Dr.D.Sridharan Addl. Director, CFD

# **Co-ordinator:**

R. Balasubramanian Asso. Prof., Dept. of EEE

Venue: EEE Seminar Hall, KS Block EEE Department

# **IMPORTANT DATES:**

Submission of Application : 13.06.2014

Intimation of Selection : 16.06.2014

Confirmation by Participants: 18.06.2014

# SARANATHAN COLLEGE OF ENGINEERING

The College was founded by philanthropist Vidya Seva Ratnam Shri K.Santhanam in the year 1998 and was named after the great educationist Prof.V.Saranathan. The campus is located on Trichy – Viralimalai – Madurai highway(about 6kms from Trichy) having an area of 40 acres with a built up area of 2,60,000 Sq.ft. Six U.G Programmes and five P.G programmes are offered. All the programmes are affiliated to the Anna University, Chennai and have the approval of the AICTE, New Delhi.

# Electrical & Electronics Engineering Department

Electrical & Electronics Engineering
Department was started in the year 1998. The
department is well equipped with modern facilities
besides conventional infrastructures. The
department has well qualified and experienced
faculty and supporting staff. The department is
accredited by National Board of Accreditation
(NBA) and recognized as research centre for
pursuing collaborative research with Anna
University, Chennai.

#### RESOURCE PERSONS

Eminent professors from academia and technical experts from industries along with our faculty members are the resource persons.

# **FDTP OBJECTIVES**

Centre for faculty Development, Anna University, Chennai trains the teachers to plan and prepare the lessons, understand the subject contents and improve the teaching quality. These programmes are conducted during the Summer / Winter Vacation to enable the teachers to participate and benefit.

### **COURSE COVERAGE**

Apart from covering the theoretical concepts of Power Electronics, hands-on laboratory exposure will be given to the participants. This is aiming at understanding the concepts clearly as well as training themselves for conducting the Power Electronics Lab as per regulation 2013 with discrete components as a viable alternative to the readily available kits. Topics to be covered are as follows

- Power Semiconductor devices overview and characteristics
- Controlled Rectifiers
- Choppers
- Inverters
- AC voltage controllers
- Cycloconverters.